

S-1)

assign-homes :  $X$  (vars)  $\rightarrow X$  (no vars)

- free stack is vast, but it is slow

- registers are few and fast

$\hookrightarrow (< 16)$

(let v := 1 in

(let w := 46 in

(let x := v + 7 in

(let y := 4 + x in

(let z := x + w in

(+ z (-y)))))))

D-2]

assignments to  
regs as we  
see them

movq \$1, !v // let v:=1 {v}  $\rightarrow \emptyset$

movq \$46, !w // let w:=46 {v, w}

live set =

movq !v, !x {x, w}

active vars

addq \$7, !x // let x = v+7 {x, w}

movq !x, !y {x, w, y}

addq \$y, !y // let y = y+x {x, w, y}

movq !x, !z {w, y, z}

addq !w, !z // let z = x+w {y, z}

movq !y, !t {t, z}

negq !t // (- y) {t, z}

movq !z, %rax {t, %rax}

addq !t, %rax // (+ z +) {y<sub>max</sub>}  $\approx$  live-after

S-3]

insts io ... in or  $\Sigma^{\text{max}}$

LiveAfter ( $k$ ) =  $\emptyset$  if  $k = n$   
LiveBefore ( $k+1$ ) o.w.

LiveBefore ( $k$ ) = (LiveAfter ( $k$ ) \setminus w(k)) \cup R(k)

$w$ : inst  $\rightarrow$  set(vars and reg) we iterate

$R$ : inst  $\rightarrow$  set(vars and reg) we read

$m$ : arg  $\Rightarrow$  set(vars and reg)

$$w(\text{popq } a) = m(a)$$

$$m \circ n = \Sigma^{\text{reg}}$$

$$R(\text{popq } a) = \emptyset$$

$$m \circ r = \Sigma^{\text{reg}}$$

$$w(\text{pushq } a) = \emptyset$$

$$m \circ n = \emptyset$$

$$R(\text{pushq } a) = m(a)$$

J-y/

$$w(\text{negg } a) = M(a)$$

$$R(\text{negg } a) = M(a)$$

$$w(\text{addg } \text{src}, \text{dst}) = M(\text{dst})$$

$$R(\text{addg } \text{src}, \text{dst}) = M(\text{src}) \cup M(\text{dst})$$

$$w(\text{mavg } \text{src}, \text{dst}) = M(\text{dst})$$

$$R(\text{mavg } \text{src}, \text{dst}) = M(\text{src})$$

$$\text{mavg } !x, !z \quad \text{L} = \{w, y, z\}$$

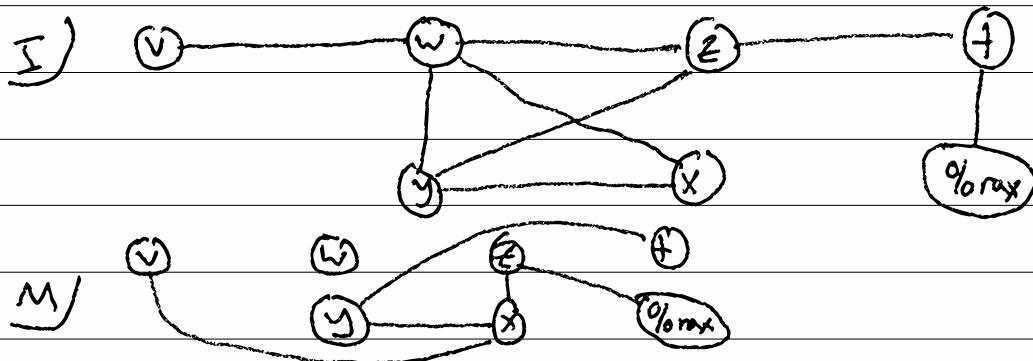
$$w = \{z\} \quad R = \{x\}$$

$$\begin{aligned} & (\{w, y, z\} \setminus \{z\}) \cup \{x\} \\ &= \{x, y, w\} \end{aligned}$$

S-5) interference : When we need two vars at  
the same time

$V = \{\text{vars and reg}\}$  edges = I

$\times I y \text{ iff } \exists k. \text{LiveAfter}(k) \ni \{x, y\}$   
move-bracing = M



5-6)

Suppose  $\text{inst}_k$  is ...

(add s d)

$\forall v \notin \text{LiveAfter}(k)$ , we'll add  $(d, v)$  to I

except for  $v = d$

(mang s d)

add  $(s, d)$  to M

$\forall v \in \text{LiveAfter}(k)$ , we'll add  $(d, v)$  to I

except for  $v = d$  or  $v = s$

If  $\text{inst}_k$  treats a ~~register~~ "second" then add

$(r, r)$  to I for all  $r \in \text{LiveAfter}(k)$

callig if treats caller-saved reg special and %rax

↳ rax, rcx, rcx, rsi, rdi, r8-r11