

4-1)

uncover-locals :  $C \rightarrow C$  (removes vars)

before info :  $\emptyset$

after info : set of vars

$\{x, y, z, a, b, c\}$

uncover (program info  $[-max \rightarrow +]$ )

= (program info'  $[-max \rightarrow +]$ )

where info' =  $\{x_0, \dots, x_n\}$

if  $f = (\text{sig } (\text{set! } x_0 \rightarrow) \dots (\text{set! } x_n \rightarrow) a)$

4-2/

select-instrs = C  $\Rightarrow$  X (has vars and ~~blocks~~ some rules)

select (program info [main  $\Rightarrow$  +])

= (program info [main (block  $\emptyset$  (select<sub>+</sub> +))])

select<sub>+</sub> (return a) = [movg (select<sub>a</sub> a) RAX;  
(jump END)]

(seg s +) = select<sub>s</sub> s ++ select<sub>+</sub> +

select<sub>s</sub> (set! x e) = select<sub>e</sub> (select<sub>a</sub> x) e

select<sub>a</sub> (num n) = (num n)      select<sub>a</sub> (var v) = (var v)

select<sub>e</sub> dst (Arg a) = [movg (select<sub>a</sub> a), dst]

(Read) = [callq \_read\_int ; movg RAX dst]

(Neg a) = [movg (select<sub>a</sub> a) dst ; negg dst]

Add aL aR = [movg (select<sub>a</sub> aR) dst ; addg (select<sub>aL</sub> aL) dst]

4-3/

mi  $\rightarrow$  r  $\rightarrow$  opt  $\rightarrow$  r  $\rightarrow$  rco  $\rightarrow$  r  $\rightarrow$  rcon  $\rightarrow$  c  $\rightarrow$  uncore  $\rightarrow$  c  $\rightarrow$  clet  $\rightarrow$  x  $\rightarrow$  assign  $\rightarrow$  x  $\rightarrow$  patch  $\rightarrow$  x

assign-hoves : X (w/vars)  $\Rightarrow$  X (w/o vars)

assign (program (var {x<sub>1</sub> ... x<sub>n</sub>}) [main  $\Rightarrow$  (block  $\phi$  is)])

let how-many-vars = n  $\rightarrow$

stack-space = 8x (n or n+1) in

(program  $\phi$  [

~~begin~~  $\rightarrow$  [ pushq RBP; movq RSP  $\rightarrow$  RBP;  
begin subq \$8, RSP; jmp body ]

end  $\rightarrow$  [ addq \$8, RSP; popq RBP; retq ]

body  $\rightarrow$  [ assign  $\sigma$  is )  $\sigma = [x_i \mapsto \%RBP(8x_i)]$

4-4

assign  $\sigma$   $[] = []$

assign  $\sigma$   $(i : is) = a \sigma i :$   
 $\sigma is$

assign  $\sigma$   $(addq a_l, a_r) = addq$  (assign  $\sigma$   $a_l$ ), (assign  $\sigma$   $a_r$ )

$(negq a) = negq$  (assign  $\sigma$   $a$ )

assign  $\sigma$   $(num n) = (num n)$   $(reg r) = (reg r)$

$(var v) = \sigma(v)$

4-5 patch = x  $\rightarrow$  x

patch<sub>rs</sub> (cons i is) = patch<sub>h</sub> i i ++ patch<sub>s</sub> is

patch (add<sup>src</sup> R<sub>1</sub>(O<sub>1</sub>) R<sub>2</sub>(O<sub>2</sub>)<sup>dst</sup>) =  
[ mov<sup>src</sup> R<sub>1</sub>(O<sub>1</sub>) , tmp-reg ;  
add<sup>dst</sup> tmp , R<sub>2</sub>(O<sub>2</sub>) ]

(mov<sup>src</sup> R<sub>1</sub>(O<sub>1</sub>) R<sub>2</sub>(O<sub>2</sub>)<sup>dst</sup>) =  
[ mov<sup>src</sup> R<sub>1</sub>(O<sub>1</sub>) , tmp ;  
mov<sup>dst</sup> tmp , R<sub>2</sub>(O<sub>2</sub>) ]

i = [ i ]

4-6 / runtime.c

```
int read_int() { int x; scanf("%d", &x); return x; }  
void print_int() { printf("%d", x); return 0; }
```

main: x → x

(program - blks) = (program - (blks + +

[ -main → (block ∅

[ callq BEGIN;

movq RAX, RDI;

callq -print\_int

retq ] ]

4-7/

test-on-real-hardware :  $X \rightarrow \text{Num}$

write (emit  $x$ ) " $x.s$ "

exec " $cc \text{ runtime.c } x.s -o x.bin$ "

exec " $./x.bin$ "  $\rightarrow$  ans-str

let ans = str  $\rightarrow$  num ans-str

return ans

150!