

3-1/

$$R_0 \xrightarrow{\text{uniquity}} R_0 \xrightarrow{\text{flatten}} C_0 \xrightarrow{\text{select}} X_0^* \xrightarrow{\text{assign}} X_0 \xrightarrow{fix} x_0$$

$$R_0.e = \text{int} \mid (\text{read}) \mid (-e) \mid (+ee) \mid \text{var} \mid (\text{let var } e)$$

$$p = (\text{program } e)$$

$$C_0.p = (\text{program } (\text{var}^*)) \text{ stmt}^* \text{ arg} = \langle \text{var}^*, \text{stmt}^*, \text{arg} \rangle$$

$$\text{stmt} = (:= \text{var exp})$$

$$\text{exp} = \text{arg} \mid (\text{read}) \mid (-\text{arg}) \mid (+\text{arg arg})$$

$$\text{arg} = \text{int} \mid \text{var}$$

$$\text{flatten} : R_0.p \Rightarrow C_0.p$$

$$\text{flatten } e : R_0.e \Rightarrow C_0.p$$

$$F(\text{int}) = \langle \emptyset, \dots, \text{int} \rangle$$

$$F(-e) =$$

$$\cancel{\langle \{nv\}, \text{av} := (-e), nv \rangle}$$

$$\text{let } \langle vs, ss, earg \rangle = F(e)$$

$$\langle nv ++ vs, ss ++ (:= nv (- earg)), nv \rangle$$

$$F(-(-8)) = \langle \emptyset, \dots, \cancel{\text{int}} \rangle$$

$$\langle nv, (:= nv (-8)), nv \rangle$$

$$\langle nv_1, nv_2, (:= nv_1 (-8)) (:= nv_2 (-nv_1)), nv_2 \rangle$$

$$F(\text{read}) = \langle nv, (nv := (\text{read})), nv \rangle$$

$$F(+e_1 e_2) = \langle vs_1, ss_1, ea_1 \rangle = F(e_1)$$

$$\langle vs_2, ss_2, ea_2 \rangle = F(e_2)$$

$$\langle vs_1 ++ vs_2 ++ av, ss_1 ++ ss_2 ++ (av := (+ ea_1 ea_2)), av \rangle$$

$$F(\text{var}) = \langle \{\text{var}\}, \dots, \text{var} \rangle$$

$$F(\text{let } x \text{ } e_1 \text{ } e_2) = \langle vs_1 ++ vs_2 ++ \{x\}, ss_1 ++ \cancel{ea_1} (:= x ea_1) ++ ss_2, ea_2 \rangle$$

3-2

$X_0 = \text{assembly}$

$X_0.p = \text{instr}^*$

$X_0.\text{instr} = (\text{addq arg arg})$	(pushq arg)
(movq arg arg)	(popq arg)
(callq label)	(subq arg arg)
(negq arg)	
(retq arg)	

$\text{arg} = \text{int} \mid \text{reg} \mid (\text{reg}, \text{offset})$

$X_0^* = X_0 \text{ but } \text{arg} = \text{int} \mid \text{var} \mid \text{reg}$

$\text{program} = \text{var}^*, X_0.p$

$\text{select} : C_0.p \rightarrow X_0^*, p$

$S(<vs, ss, a>) = <vs, SS(ss) ++ (\text{retq } a)>$

$SS(mt) = mt$

$SS(\text{stmt} :: \text{more}) = S1(\text{stmt}) ++ SS(\text{more})$

$S1 : C_0.\text{stmt} \rightarrow X_0^*, \text{$instr}^*$

$S1(x := e) =$

match on e

arg  $\Rightarrow$  (movq arg . x)

read  $\Rightarrow$  (callq \_read)

(movq rax x)

(- arg)  $\Rightarrow$  (movq arg x)

(negq x)

(+ arg, arg<sub>2</sub>)  $\Rightarrow$  (movq arg<sub>1</sub> x)

(addq arg<sub>2</sub> x)

3-3/

assign:  $X_0^*$   $\rightarrow X_0$

$\langle \text{vars}^*, \cancel{\text{instr}} + \rangle \xrightarrow{\quad / \backslash \quad} \text{instr} +$   
mention variables

$X_0^*, \text{arg} = \text{int} \mid \text{var} \mid \text{reg}$

$X_0, \text{arg} = \text{int} \mid \text{reg} \mid (\text{reg}, \text{offset})$

assign ( $\langle \text{vars}, \cancel{\text{instrs}} \rangle$ ) =  
 $(\text{subq } (* 8 \mid \text{vars} \mid) \text{ rsp})$   
~~assign ( $\cancel{\text{instrs}}$ )~~  
~~&  $\cancel{\text{args}}$~~   
 $= \text{aintr ... (retg arg)}$   
 $\downarrow$   
 $\sigma$

$\text{aintr ... (movq arg rax)}$

$(\text{addq } (* 8 \mid \text{vars} \mid) \text{ rsp})$

$(\text{retg } \# \text{rax})$

$\sigma(\text{var}) = \text{offset}$

assign\*:  $(\text{var} \rightarrow \text{offset}) \text{ instr} \rightarrow \text{instr}$

assign\* ( $\sigma, \text{var } v$ ) =  $(\text{rsp}, -8 * \sigma(v))$

fix :  $X_0 \rightarrow X_0$

movq 10, x       $\Leftarrow$       (+ y (-10))  
 negg x  
 movg x, y       $\xrightarrow{\text{assign}}$       movq 10, rsp(-8)  
 addq 52, y      negg rsp(-8)  
~~movg~~ negg y      movg rsp(-8), rsp(-16)       $\leftarrow$  illegal!  
 addq 52, rsp(-16)  
~~negg~~ movg rsp(-16), rax  
 negg rax

(cannot) two memory references in one instruction

$\begin{cases} \text{movq} & a_1 \quad a_2) \quad \text{if} \quad a_1 = \text{rsp}(n) \\ \text{addq} & a_1 \quad a_2) \quad a_2 = \text{rsp}(m) \end{cases}$

0	src	dest
(0	$\text{rsp}(n)$	$\text{rsp}(m)$ )

$\Rightarrow$

(0	$\text{rsp}(n)$	$\text{rax}$ )
(0	$\text{rax}$	$\text{rsp}(m)$ )